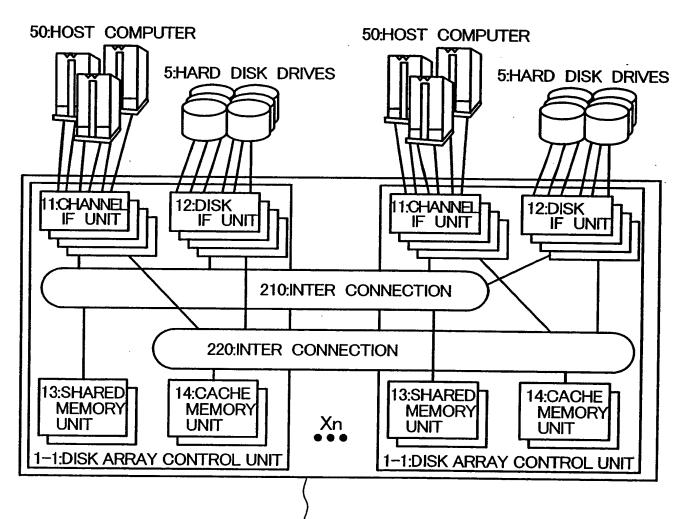
FIG.1



1:DISK ARRAY CONTROLLER

FIG.2
(PRIOR ART)

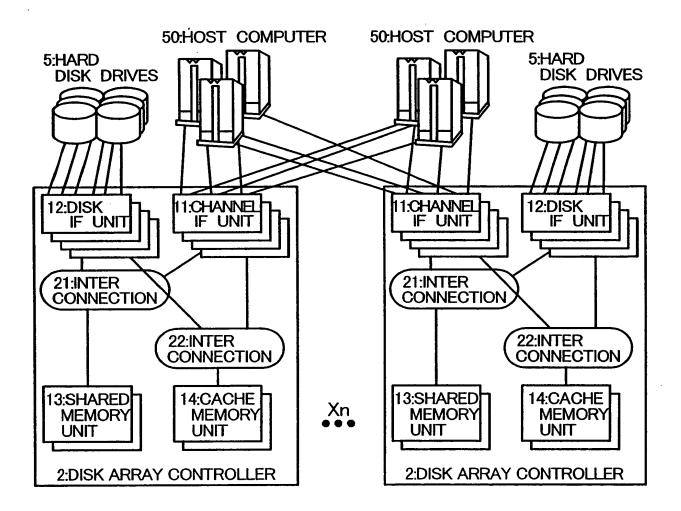


FIG.3
(PRIOR ART)

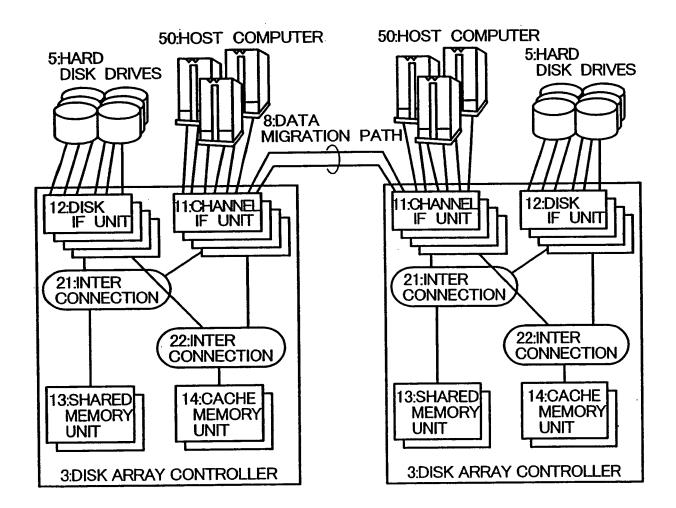


FIG.4
(PRIOR ART)

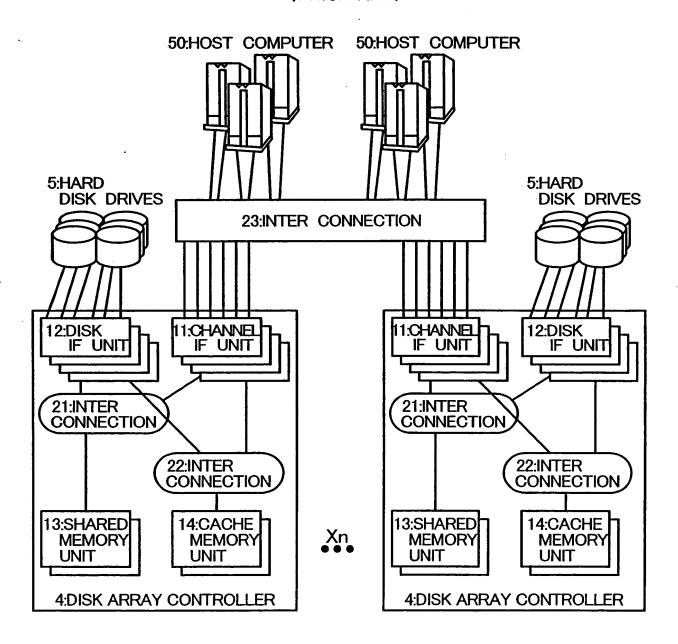
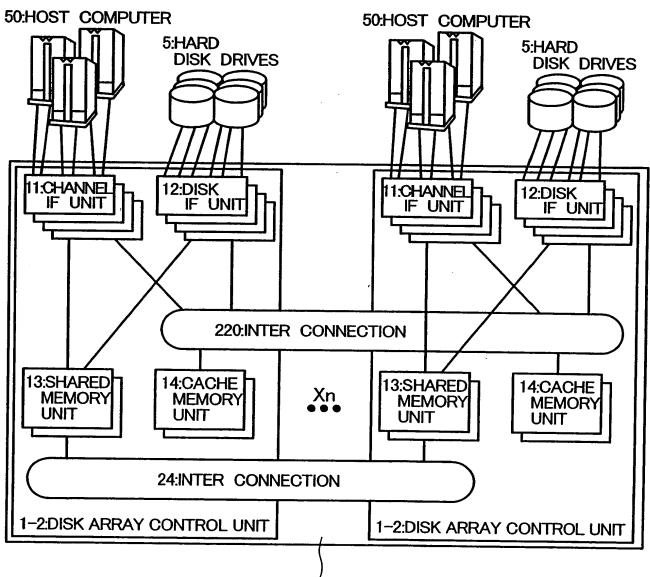
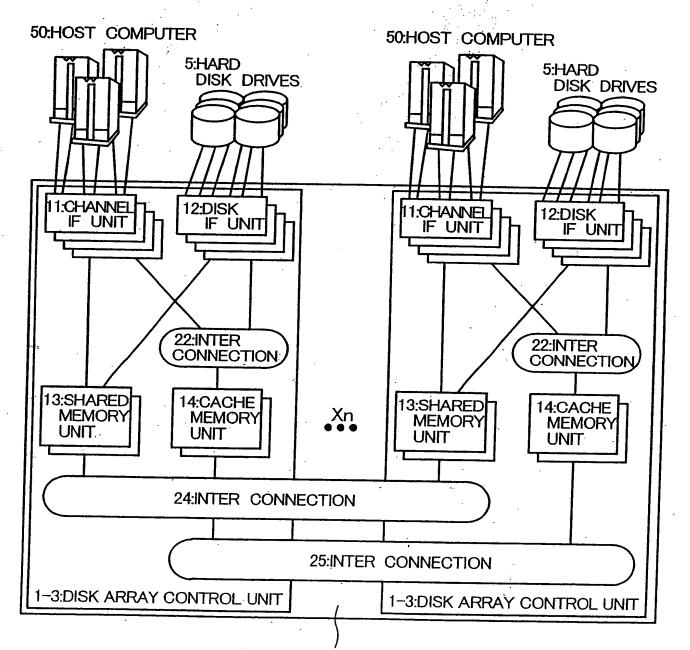


FIG.5



1:DISK ARRAY CONTROLLER

FIG.6



1:DISK ARRAY CONTROLLER

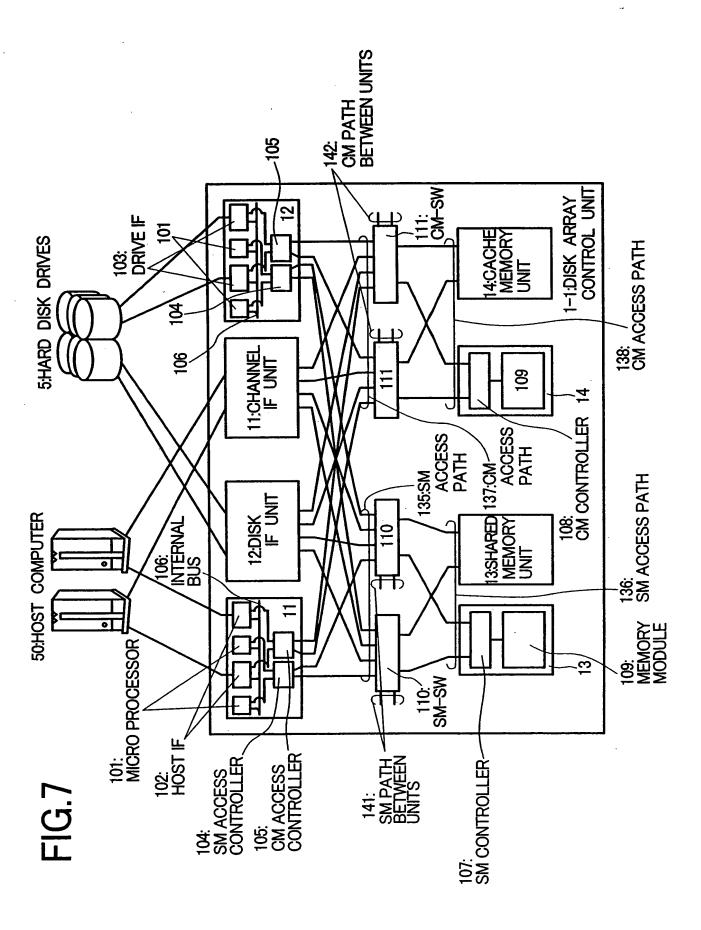
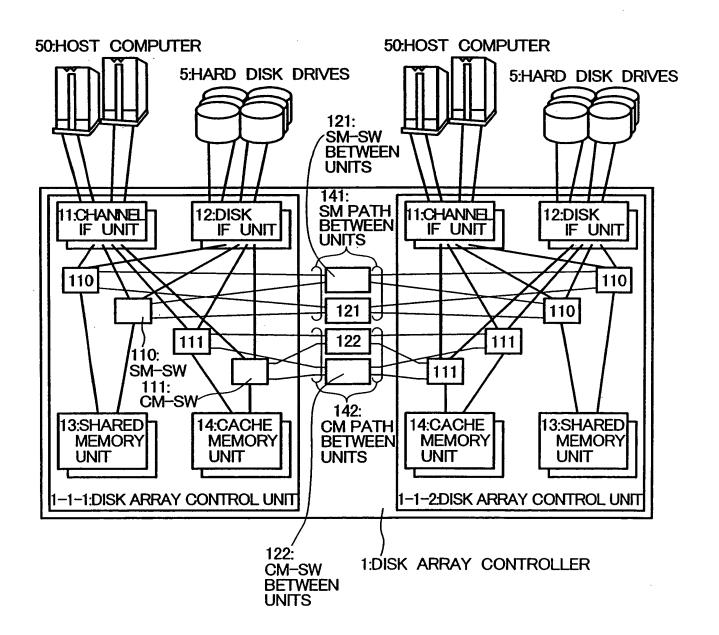
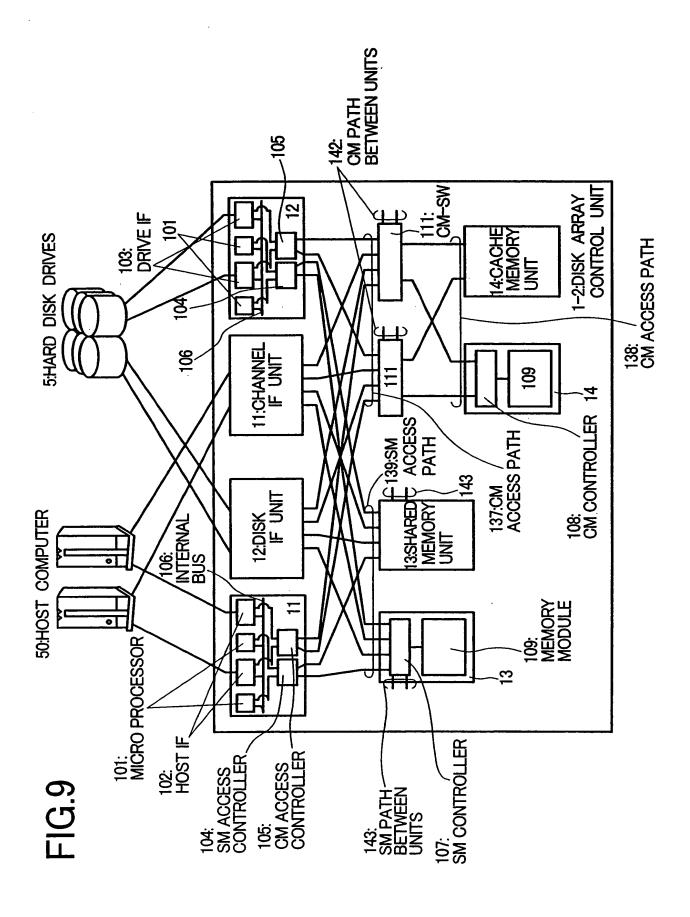
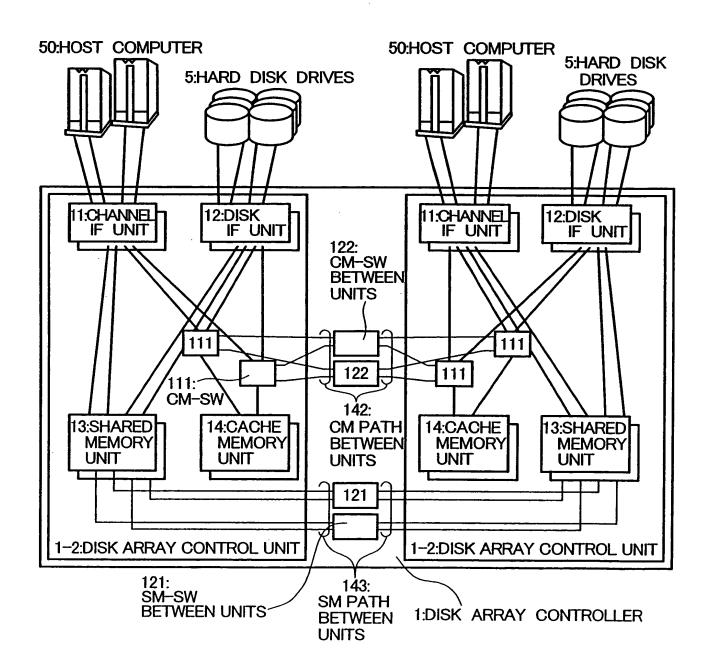


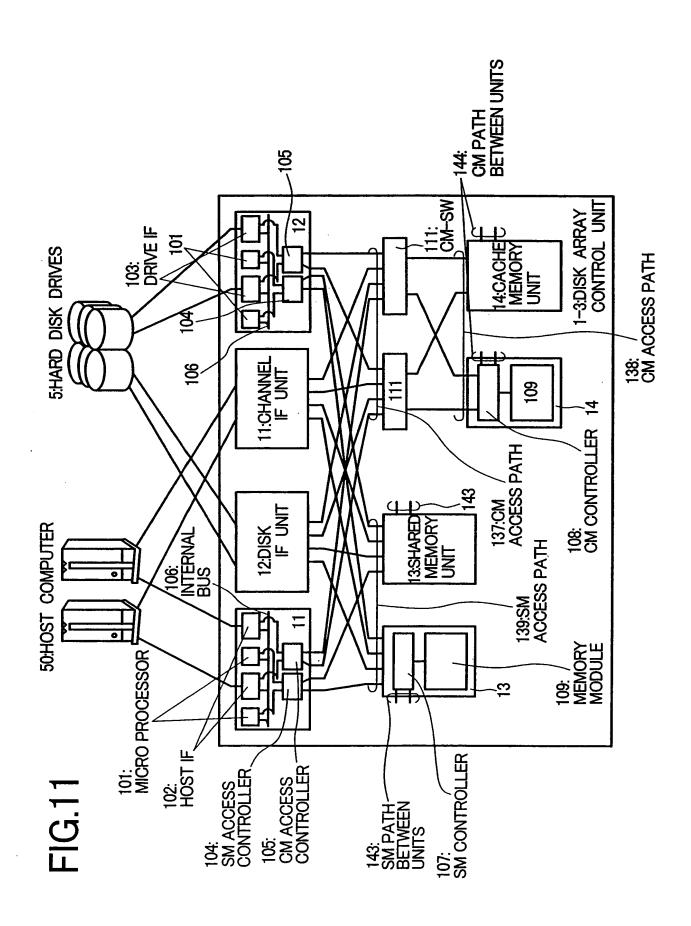
FIG.8



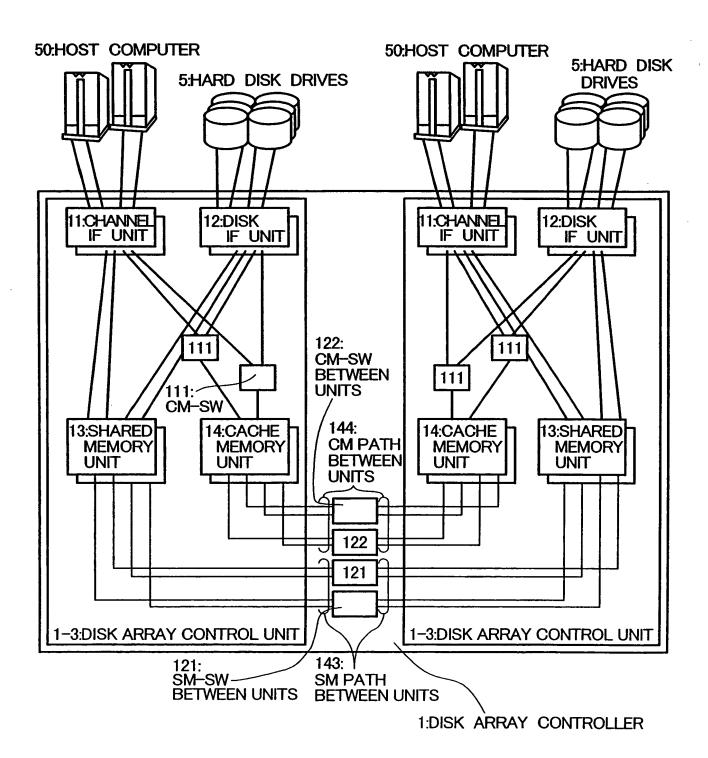


**FIG.10** 





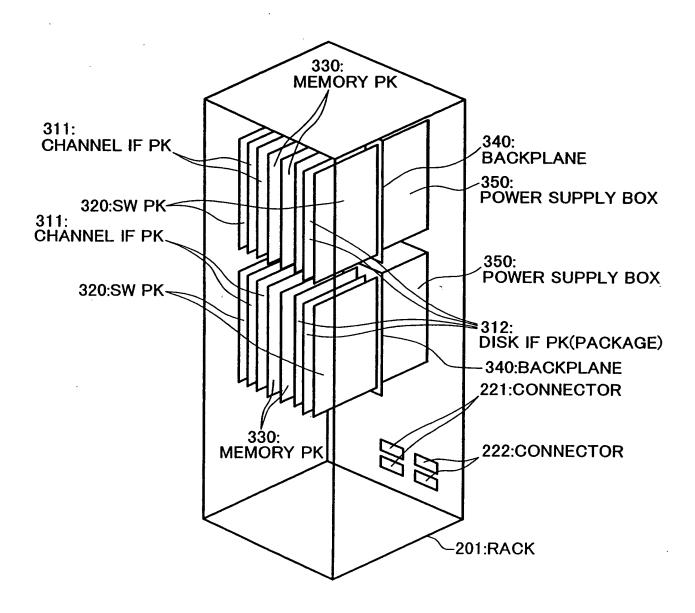
**FIG.12** 



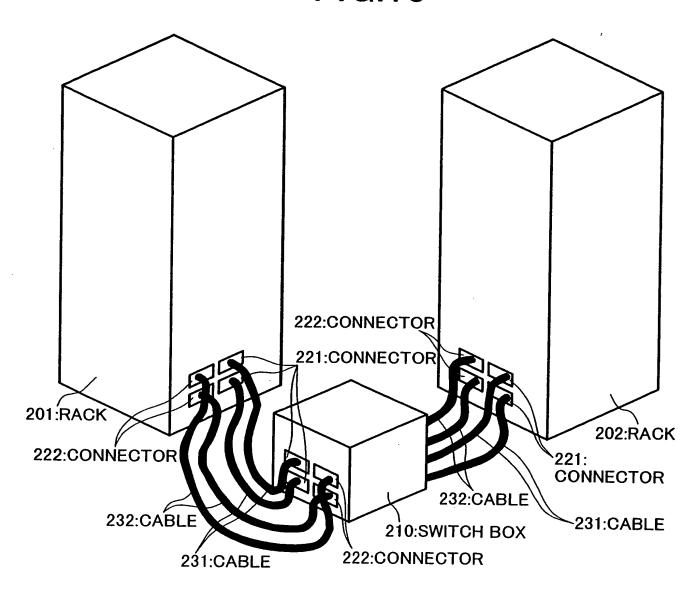
**FIG.13** 

1:DISK ARRAY CONTROLLER 1-1:DISK ARRAY 11:CHANNEL **12:DISK** CONTROL UNIT IF UNIT IF UNIT 13:SHARED 14:CACHE **MEMORY MEMORY** UNIT UNIT 110 110 111 111 12:DISK IF UNIT 11:CHANNEL 12:DISK 11:CHANNE IF UNIT IF UNIT IF UNIT 110 111 111 110: [111 110 SM-SW 110 141: CM-SW SM PATH BETWEEN 14:CACHE 13:SHARED 14:CACHE 13:SHARED **UNITS MEMORY MEMORY MEMORY MEMORY** 142: UNIT UNIT UNIT UNIT **CM PATH BETWEEN UNITS** 1-1:DISK ARRAY CONTROL UNIT 1-1:DISK ARRAY CONTROL UNIT 142: 141: **CM PATH** SM PATH BETWEEN **BETWEEN UNITS UNITS** 

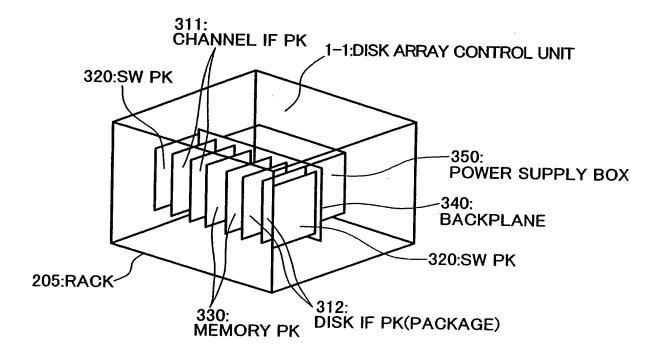
**FIG.14** 



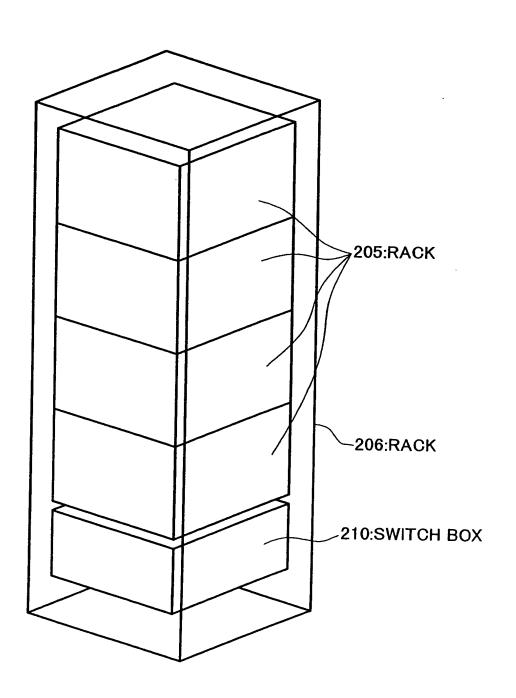
**FIG.15** 



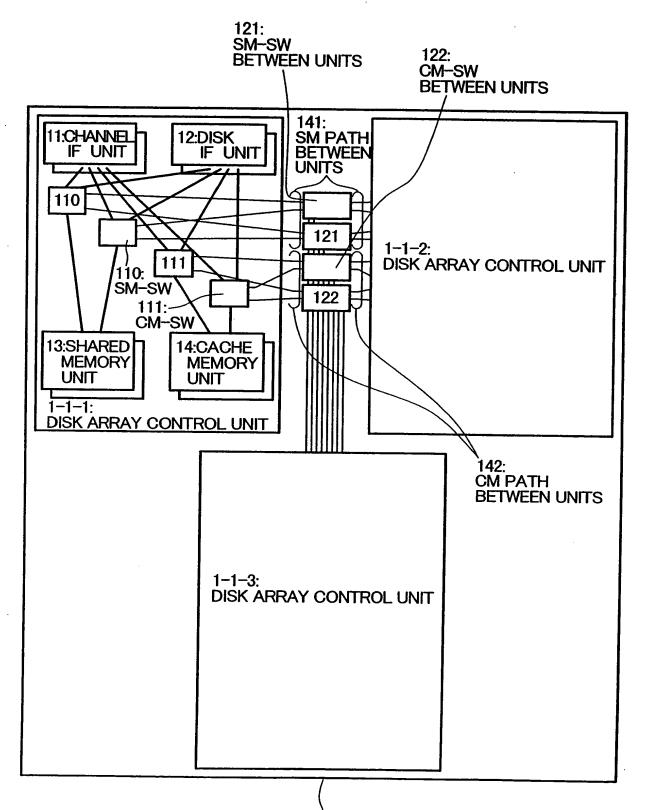
**FIG.16** 



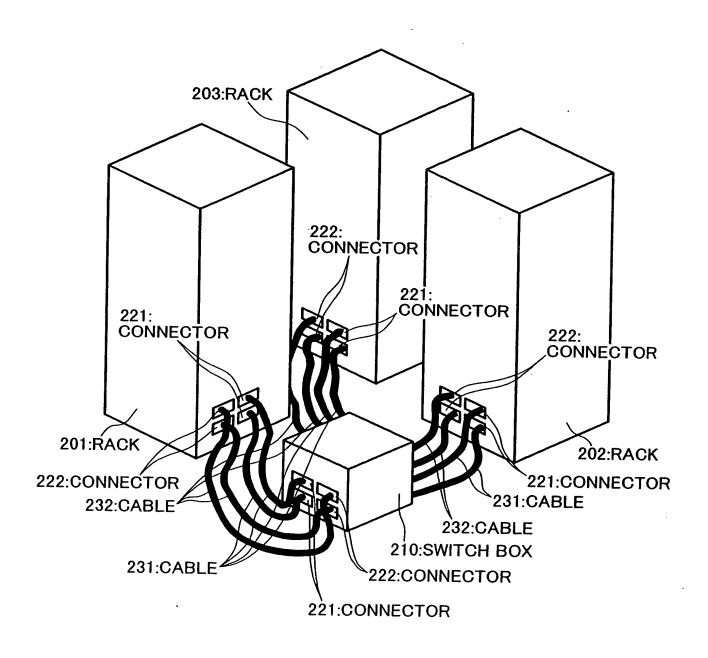
**FIG.17** 



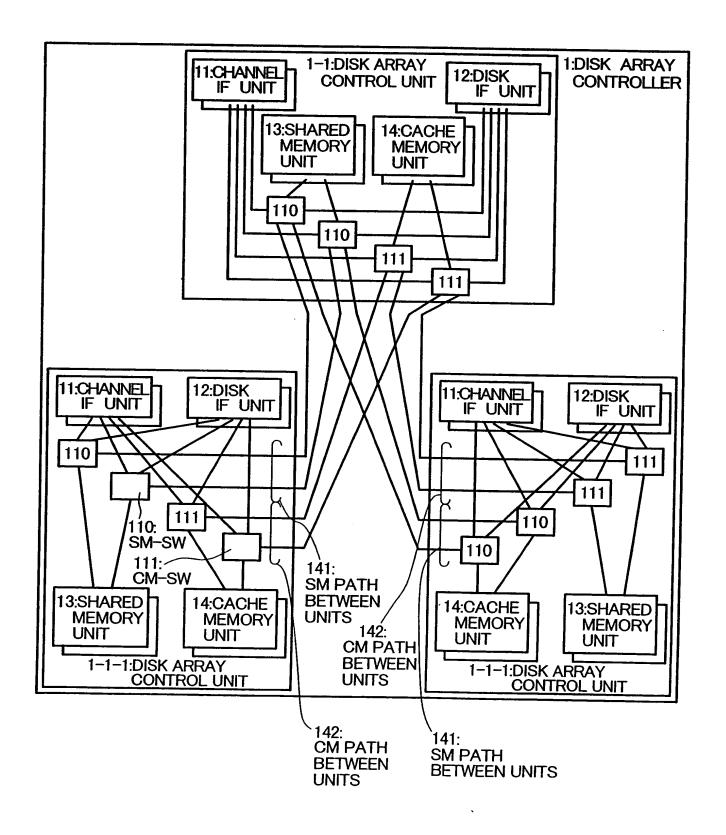
**FIG.18** 



**FIG.19** 



**FIG.20** 



**FIG.21** 

400:PACKET

401:	402:	403:
		<del>1</del> 00.
ADDRESS	ICOMMANDI	DATA
1.200	OOMINI TID	DATA

FIG.22

410:REFFERENCE TABLE FOR PATH SWITCH

ADDRESS.	PORT No.
$0 \times 0 \cdots 000$	0
$0 \times 0 \cdots 001$	0
÷	:
0 × 0 · · · OFF	0
0×0···100	1
:	:
0×0···200	2
:	: